

Notice of References Cited	Application/Control No. 10/589,109		Applicant(s)/Patent Under Reexamination STORMS ET AL.	
	Examiner BRANDON S. COLE		Art Unit 4125	Page 1 of 1

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Wikipedia - Inveter (Logic Gate)□□□□ http://en.wikipedia.org/wiki/Inverter_%28logic_gate%29			
	V	Wikipedia - Clock Signal□□□□ http://en.wikipedia.org/wiki/Clock_circuit			
	W	Wikipedia - Integrated Circuits□□□□ http://en.wikipedia.org/wiki/Integrated_circuit			
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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